

FIG. 7

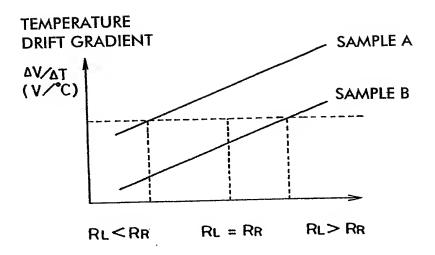
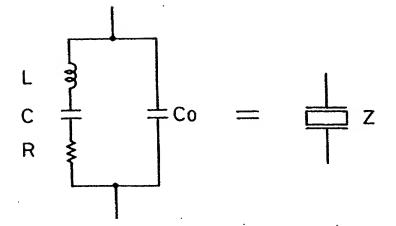
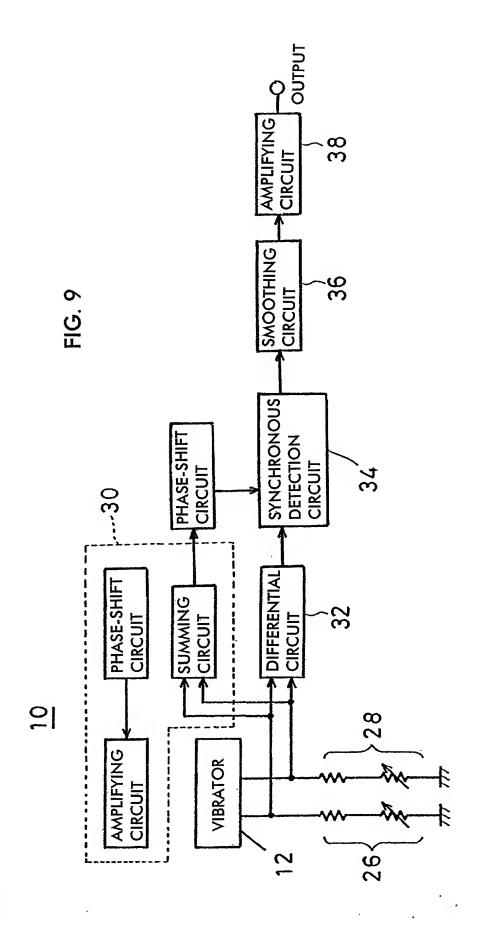
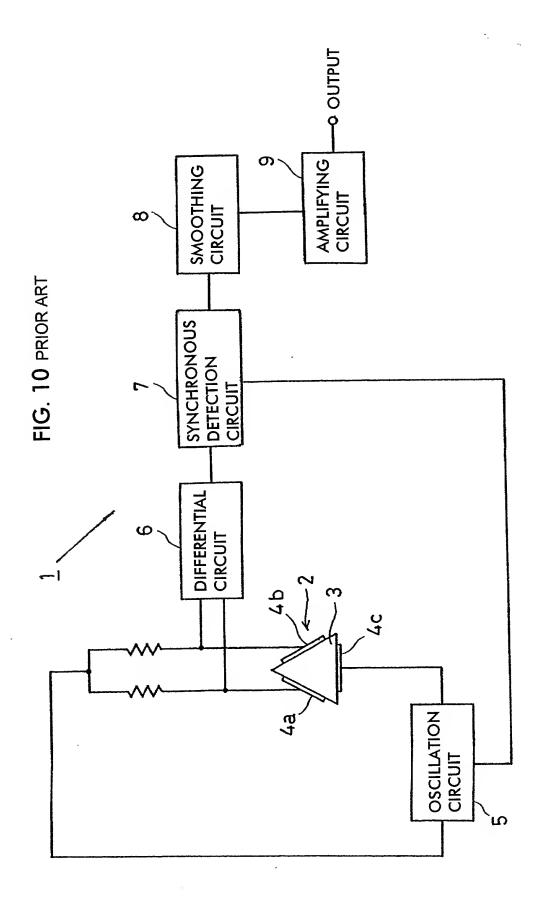


FIG. 8







SENSOR OUTPUT Ly DIFFERENTIAL AMPLIFYING

J** CIRCUIT AMPLIFYING CIRCUIT (AMPLITUDE **ADJUSTMENT**) TEMPERATURE COMPENSATION CIRCUIT SMOOTHING CIRCUIT) W SMOOTHING COMPARATOR PHASE-SHIFT CIRCUIT CIRCUIT (50°C) SYNCHRONOUS DETECTION CIRCUIT SYNCHRONOUS OSCILLATION CIRCUIT HALF-WAVE DETECTION RECTIFIER CIRCUIT DIFFERENTIAL SUMMING CIRCUIT **AMPLIFYING** CIRCUIT **ADJUSTMENT**) **ADJUSTMENT** (AMPLITUDE (AMPLITUDE BUFFER BUFFER **4**a

FIG. 11 PRIOR ART

